

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Multiple sheets used when necessary)</i>	Application No.	10/565,551
	Filing Date	January 18, 2006
	First Named Inventor	Verbauwhede et al.
	Art Unit	2877 2879
SHEET 1 OF 1	Examiner	Unknown
	Attorney Docket No.	UCLARF.003NP

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
/DC/	1	5,909,127	06-01-1999	Pearson et al.	

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Examiner Initials	Cite No.	Foreign Patent Document Country Code-Number-Kind Code Example: JP 1234567 A1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T ¹
/DC/	2	EP 0 147 598 A1	07-10-1985	International Business Machines Corporation		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
/DC/	3	TIRI ET AL., "A Dynamic and Differential CMOS Logic with Signal Independent Power Consumption to Withstand Differential Power Analysis on Smart Cards," ESSCIRC 2002, Proceedings of the 28 th European Solid-State Circuit Conference, September 24-26, 2002, Florence, Italy, 2002, pages 403-406	
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Examiner Signature	/Daniel Chang/	Date Considered	06/16/2007
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

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